

ENHANCED PARALLELISM IN TRACE SCHEDULING
BY USING RENAMING

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ABSTRACT

5 A method includes scheduling instructions within a
trace disregarding data dependencies from off trace
basic blocks. After scheduling, errors caused by
instruction movement are corrected. By disregarding
data dependencies from off trace basic blocks, more
10 parallelism is exposed resulting in more instruction
motion. In this manner, efficiency is maximized.

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